

# **An Apparatus and a Method for Securely Switching Status of a Computing System**

## **Abstract of the Disclosure**

The invention is an apparatus and methods realizing secured switch of computing system status. Explanatorily, the switch control unit responds to status switch requests and sends out NMI to CPU; CPU responds to the interrupt signals and processes  
5 secured switch control programs; through the stated switch control unit, the stated switch connection unit completes the switch connection alternatively to one of the two stated state save units; after processing the interrupt service program, the stated switch control unit notifies the stated CPU to resume the data stored in the alterable state registers of the  
10 computing system before the interrupt; thus the computing system can be safely switched between different operation systems, or internal and external networks.